



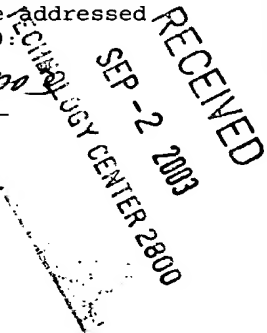
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By: 

Date: August 22, 2003



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Dieter Kantz et al.

Applic. No. : 09/826,594

Filed : April 5, 2001

Title : Test Configuration for the Functional Testing
of a Semiconductor Chip

Examiner : Ernest F. Karlson

Group Art Unit : 2829

A M E N D M E N T

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

S i r :

Responsive to the Office action dated May 22, 2003 kindly
amend the above-identified application as follows: